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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
Patent Application

Inventor(s): Sancheti et al.

Group Art Unit:

Filed: 03/18/04

Examiner:

Application No.: 10/804,988

Title: A DELAY CIRCUIT THAT SCALES WITH CLOCK CYCLE TIME

Form 1449

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
lmp	A	6,819,157	11/16/04	Cao et al.	327	262	11/15/01
lmp	B	6,445,661	09/03/02	Wu	369	59.2	08/11/99
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lmp	D	6,327,318	12/04/01	Bhullar et al.	375	374	06/30/98
lmp	E	6,175,928	01/16/01	Liu et al.	713	401	12/31/97
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lmp	G	6,043,717	03/28/00	Kurd	331	17	09/22/98

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
lmp	H	JP404227314	08/17/92	JP	H03K005	13		x

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	I	
lmp		Date Considered 10/19/2005

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered.
Include copy of this form with next communication to applicant.